

FIG.1

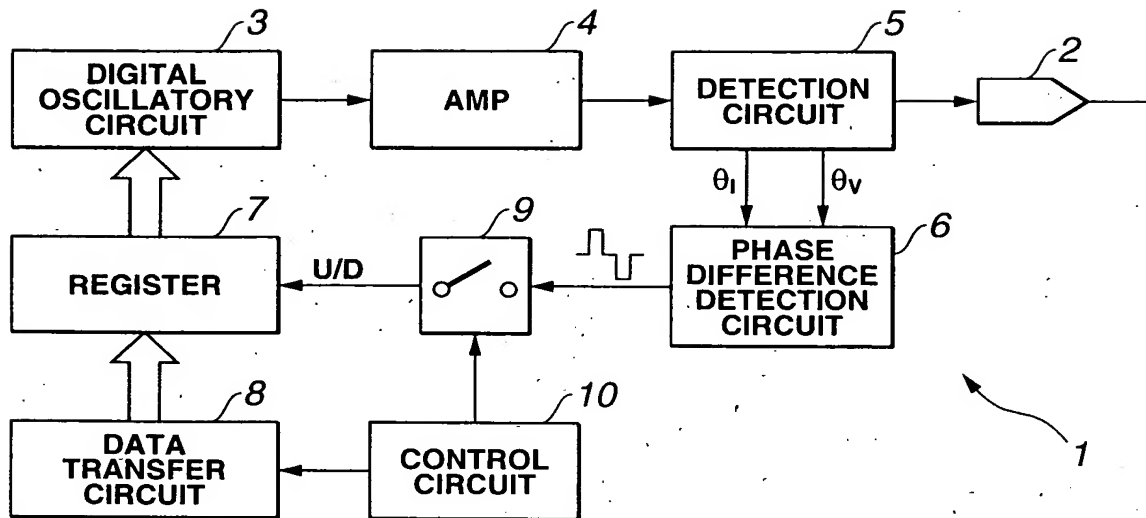


FIG.2

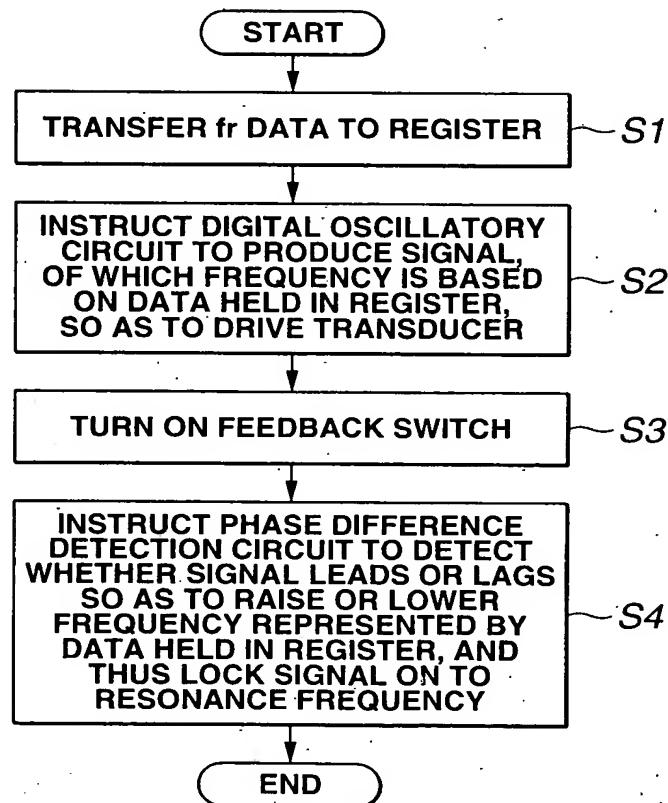
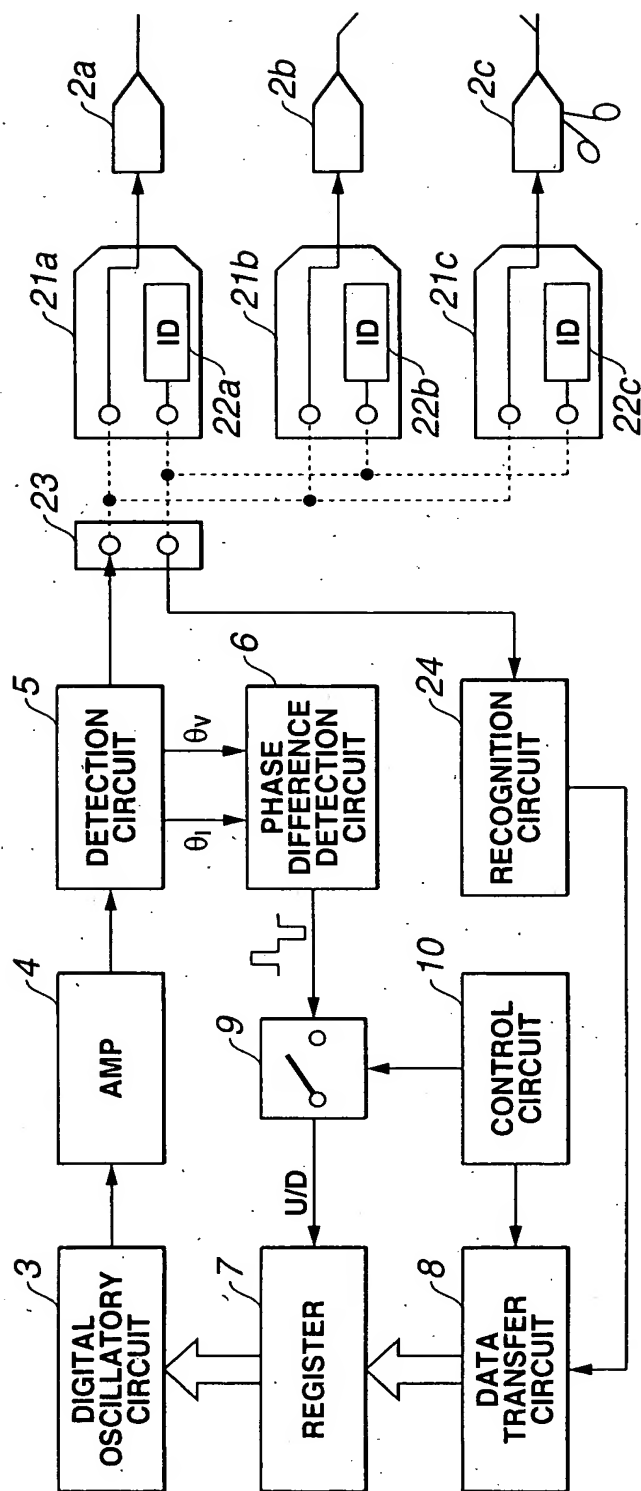


FIG.3



1

FIG.4

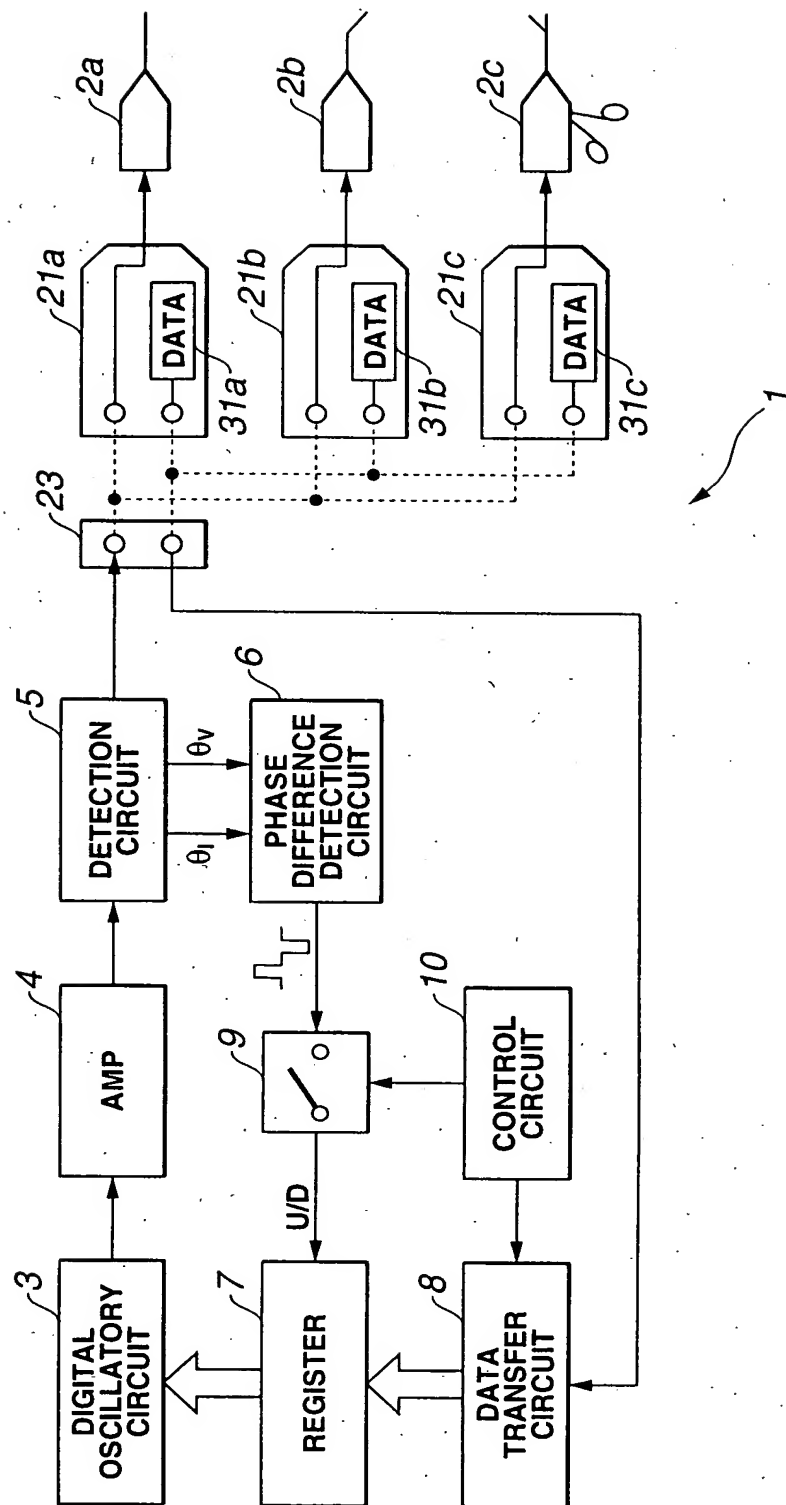


FIG.5

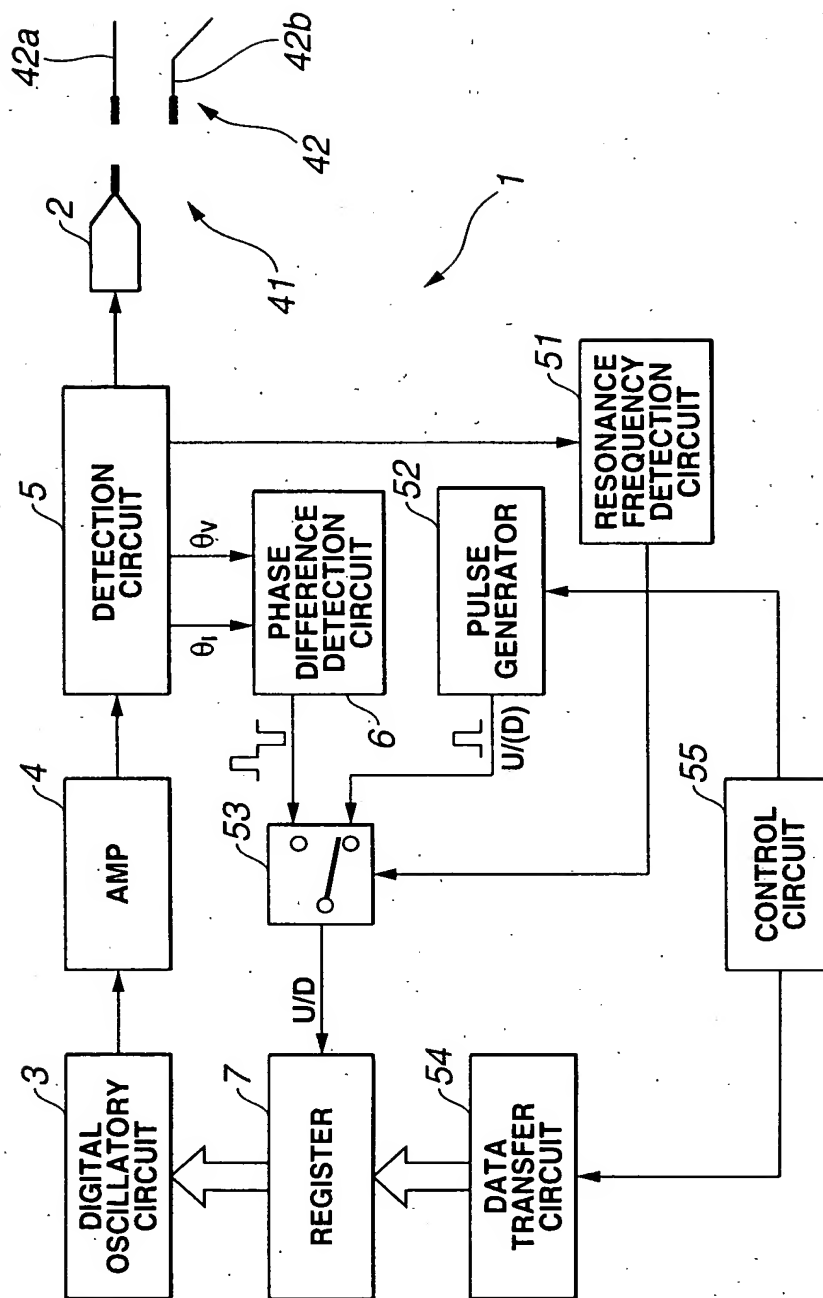


FIG.6

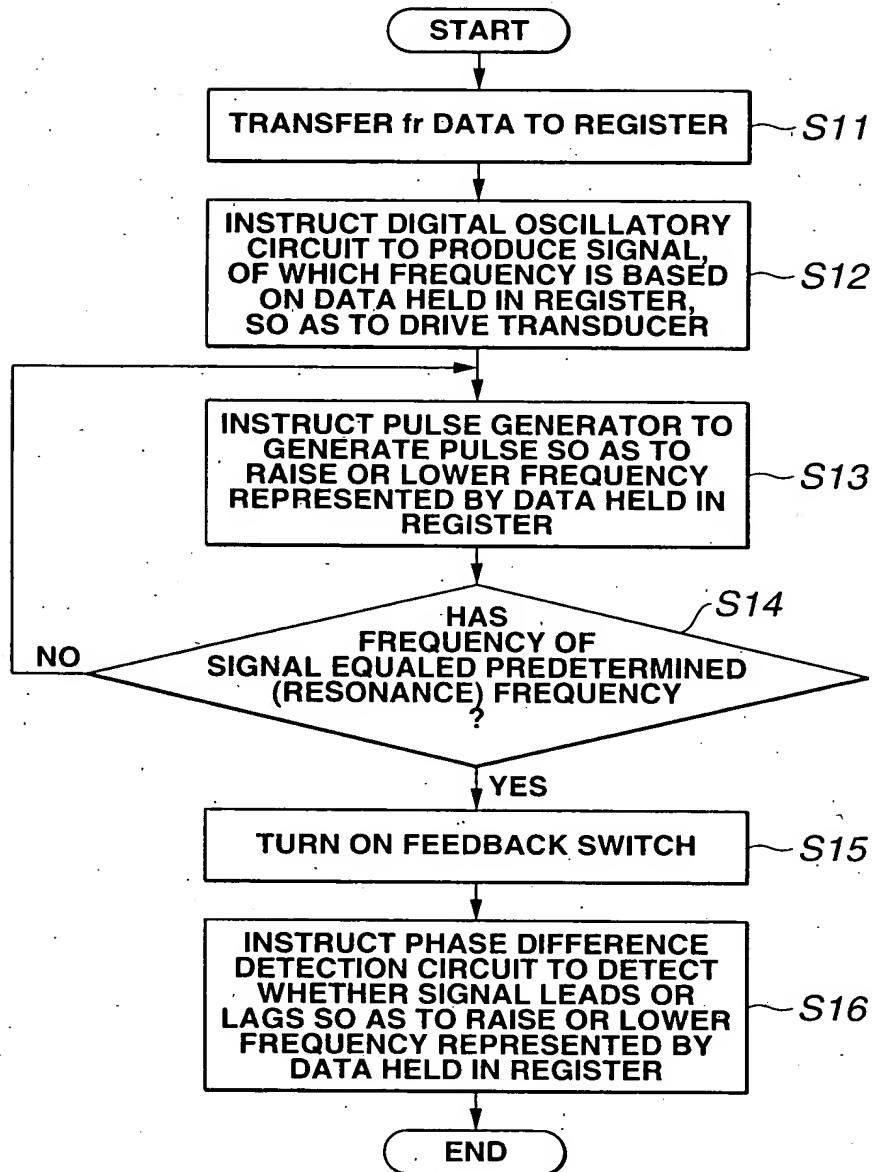


FIG.7

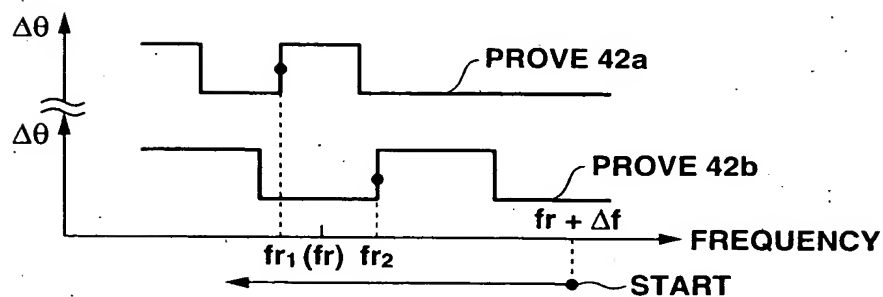


FIG.8

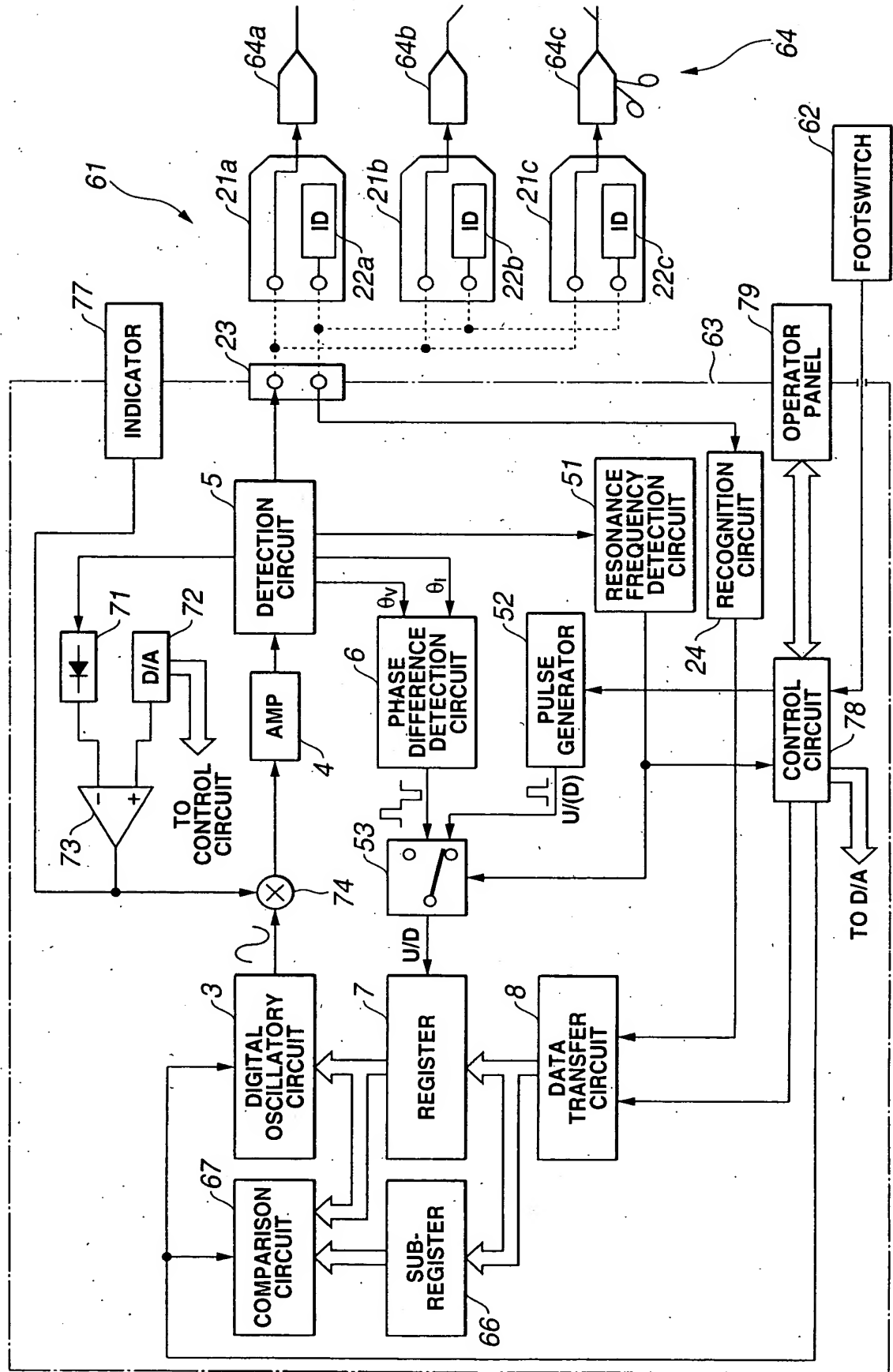


FIG.9

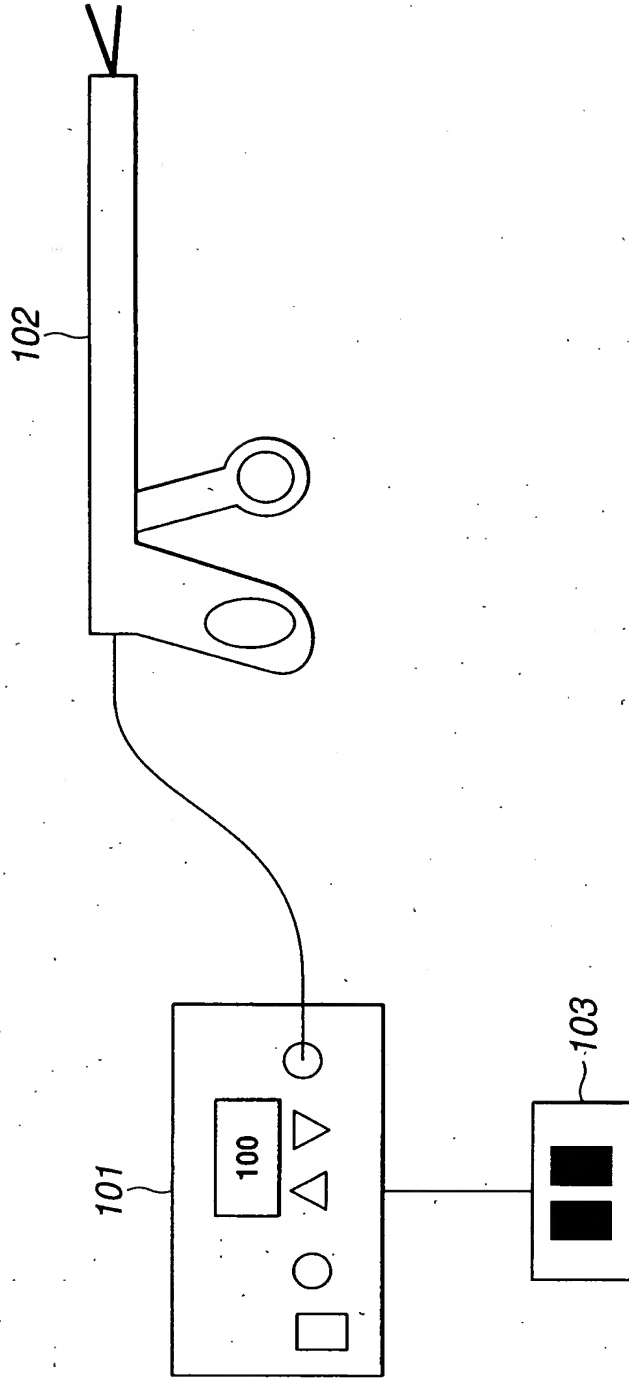


FIG.10

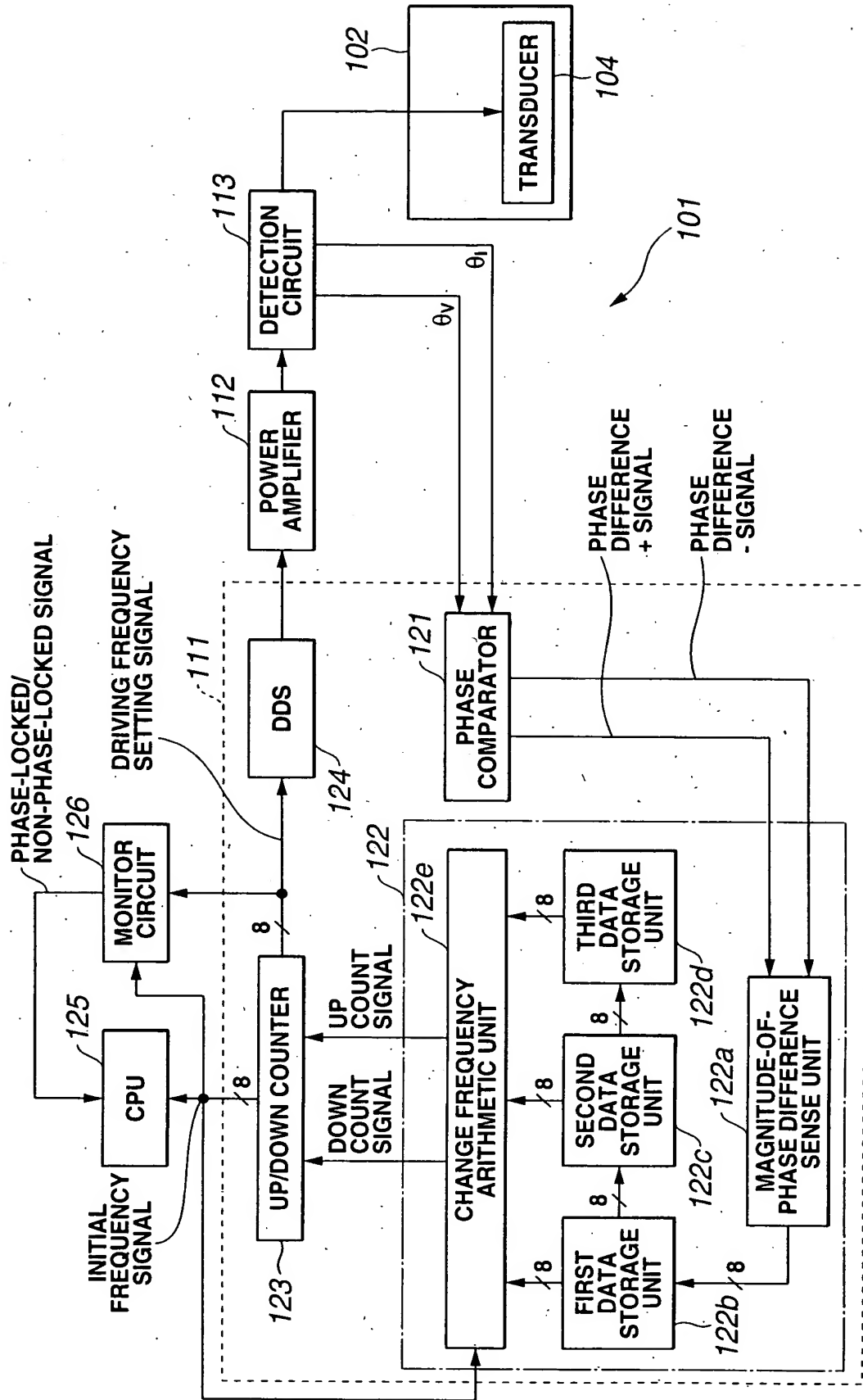


FIG.11A CLOCK



FIG.11B PHASE DIFFERENCE SIGNAL

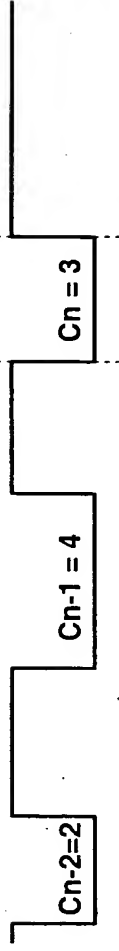
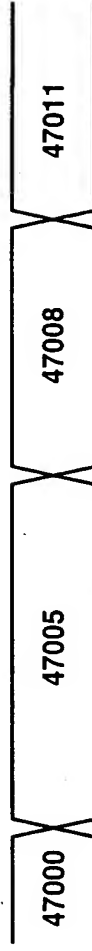


FIG.11C DRIVING FREQUENCY SETTING SIGNAL



$$(Cn-2+Cn-1+Cn)/3 = 3$$

FIG.12

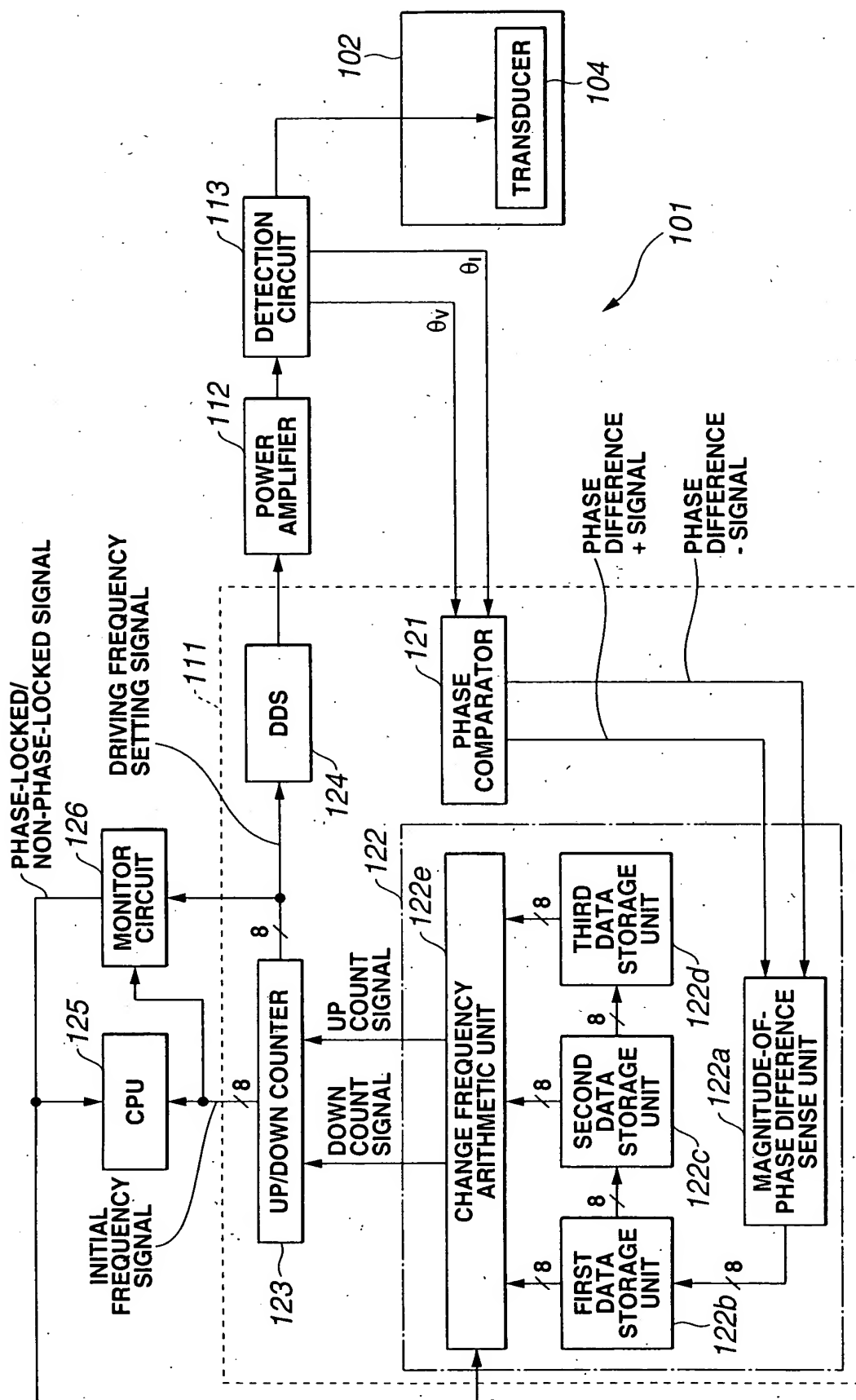


FIG. 13

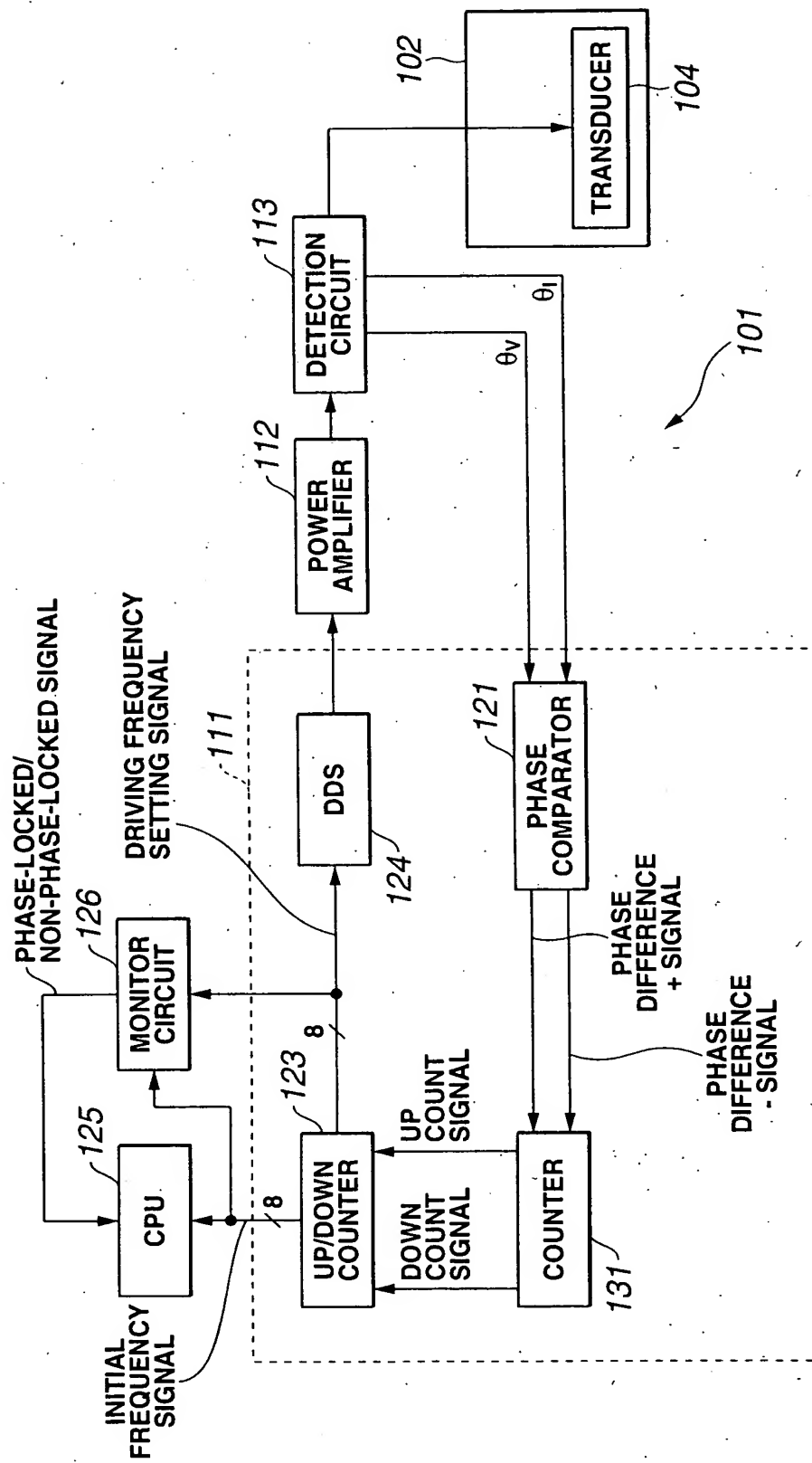


FIG. 14

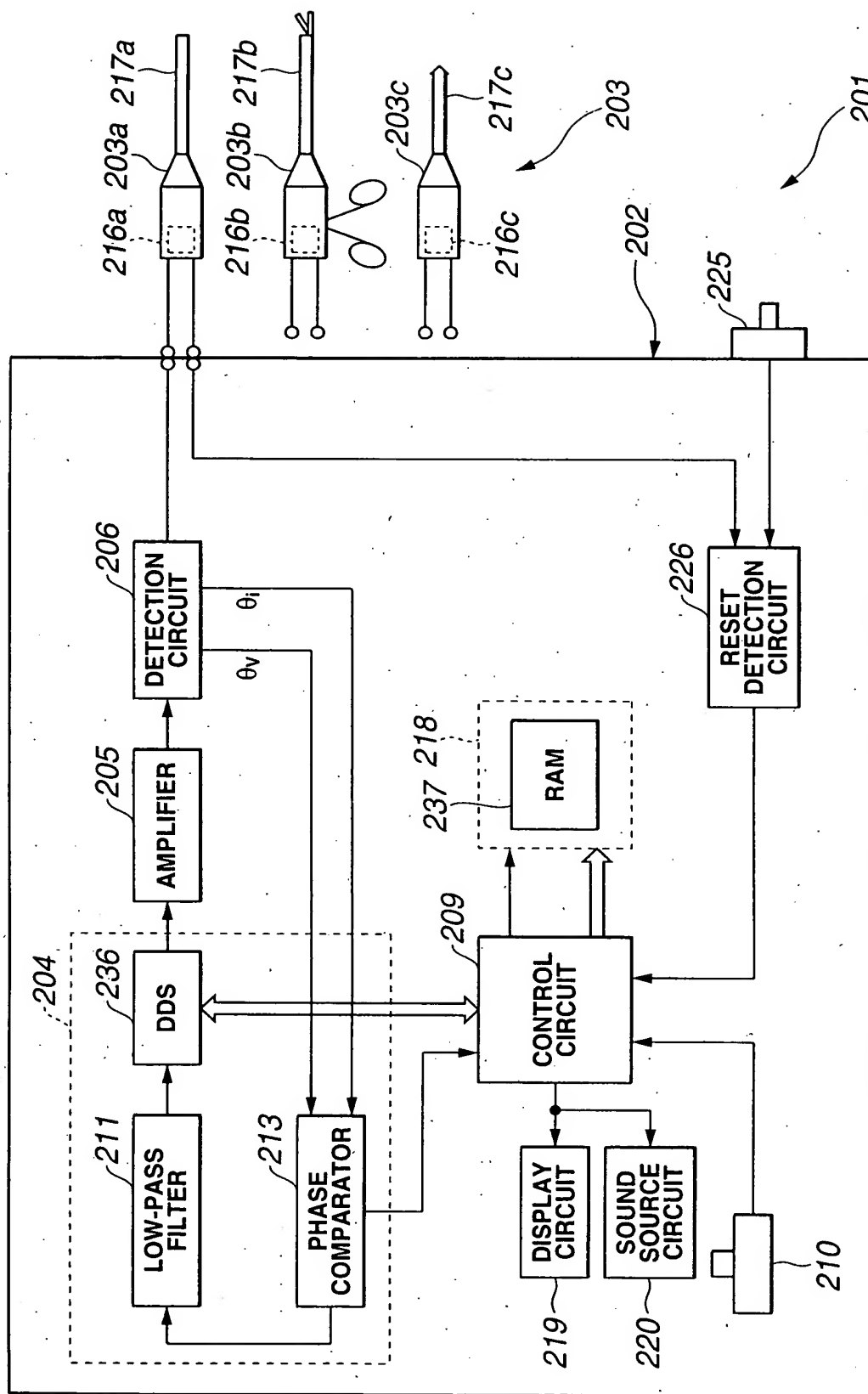


FIG.15

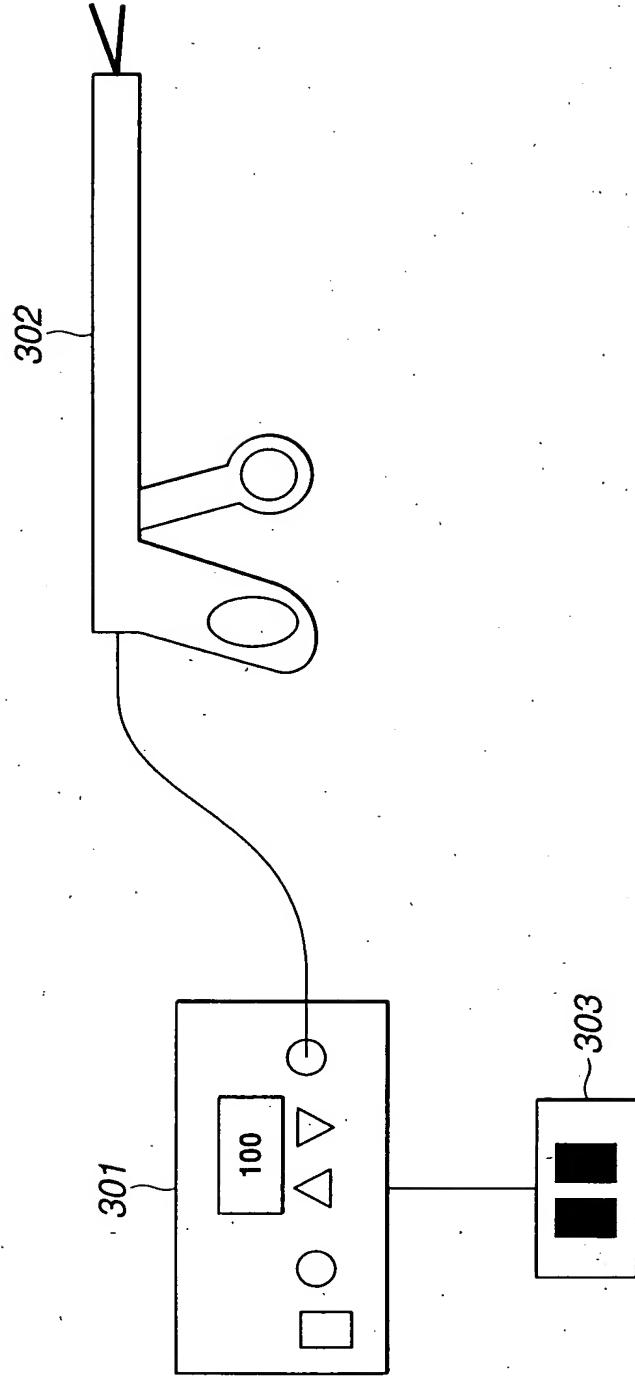


FIG. 16

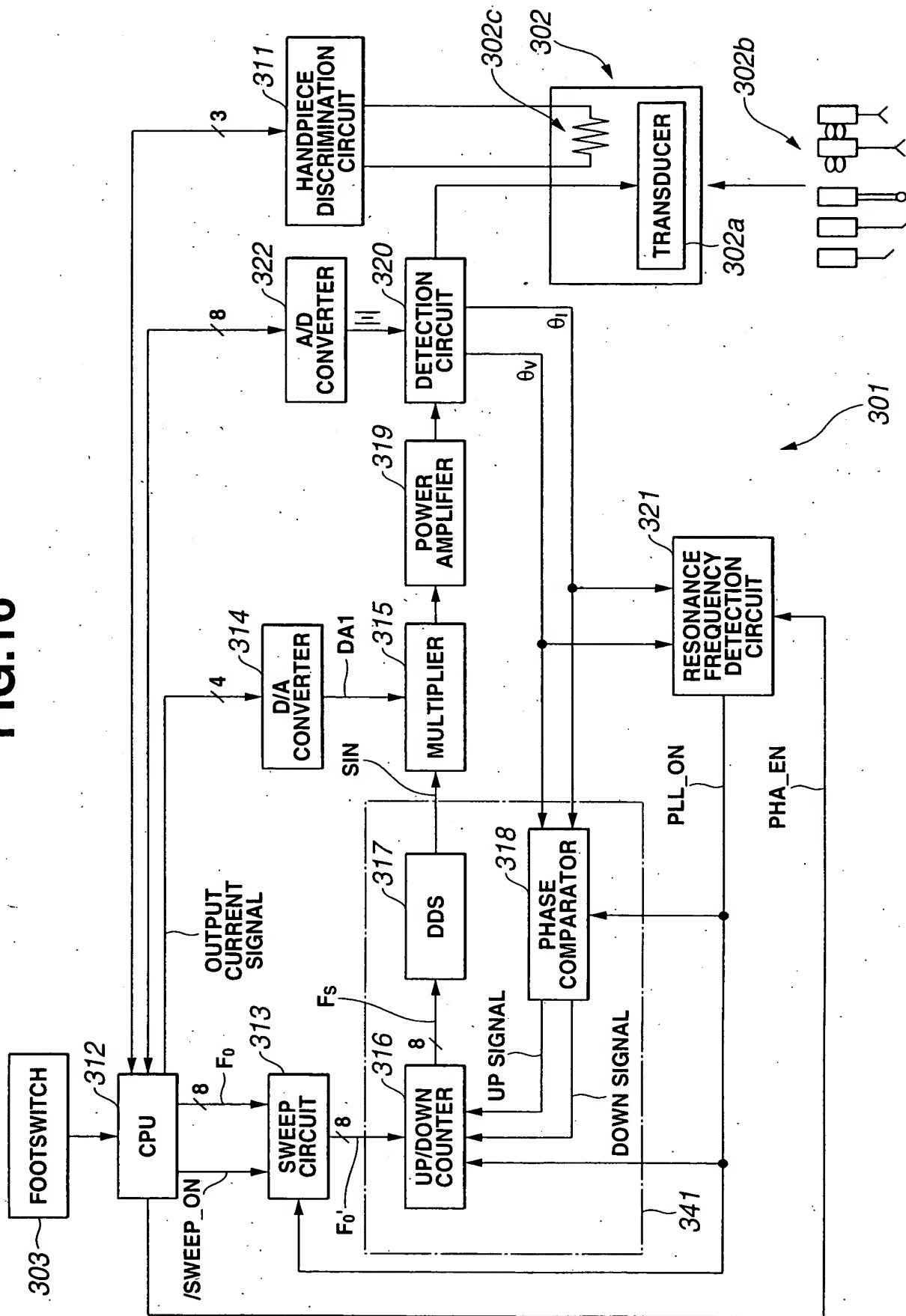


FIG.17

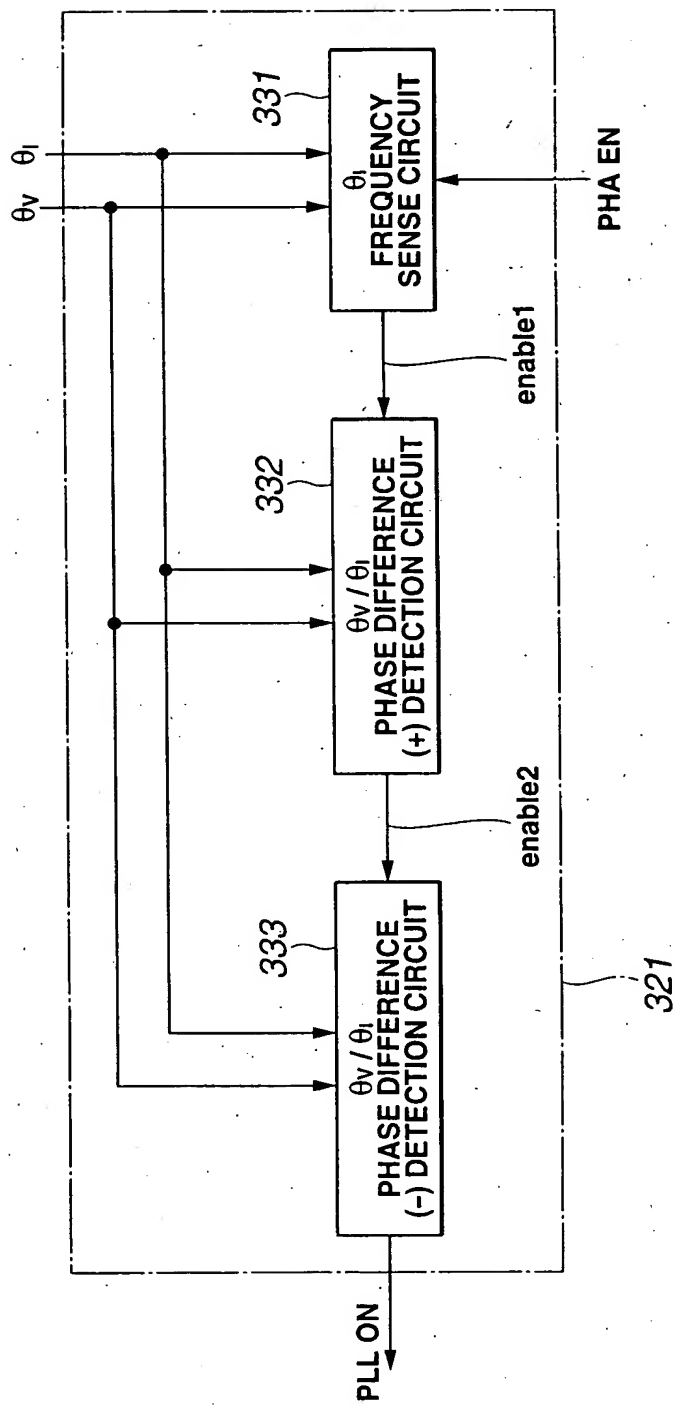


FIG.18

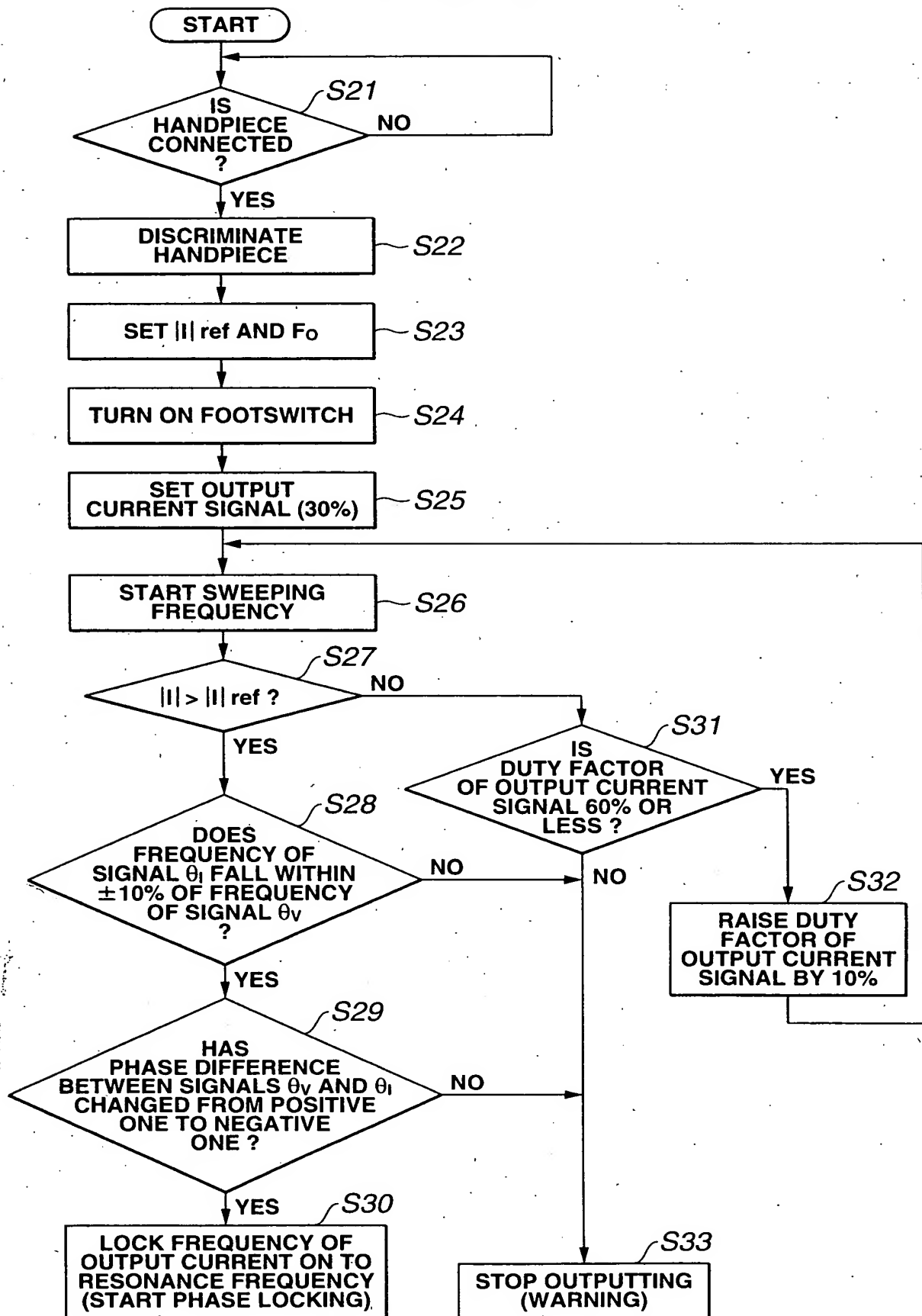


FIG.19

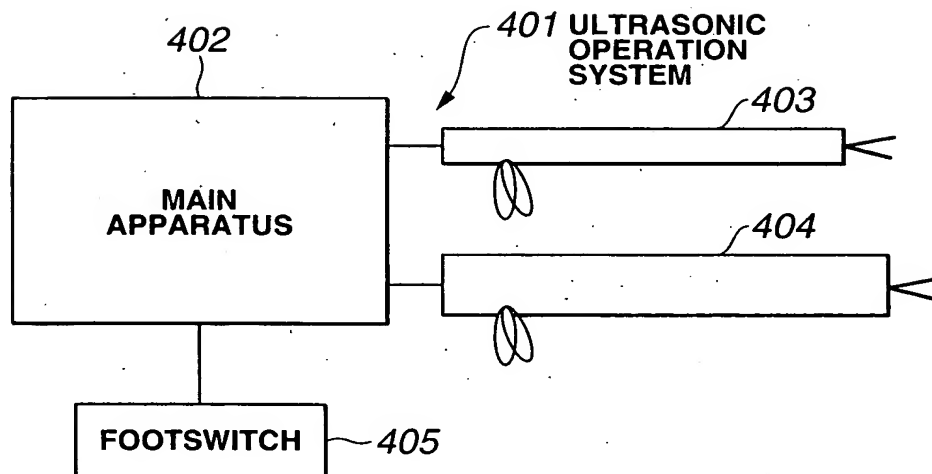


FIG.20

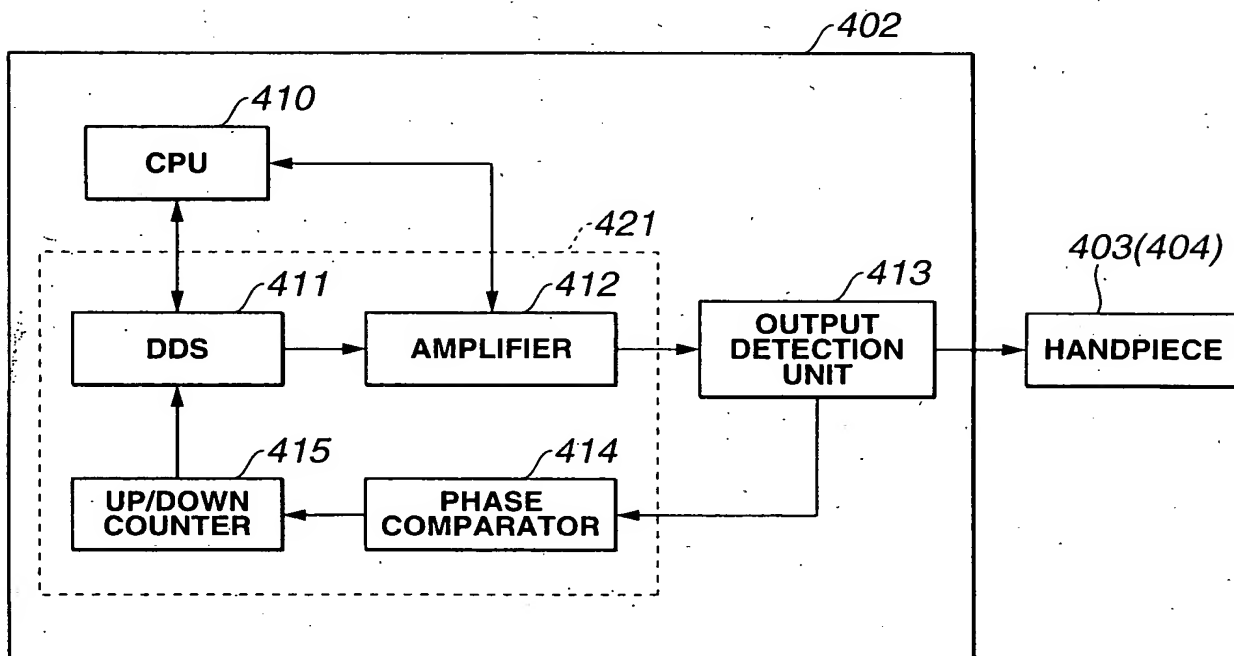


FIG.21

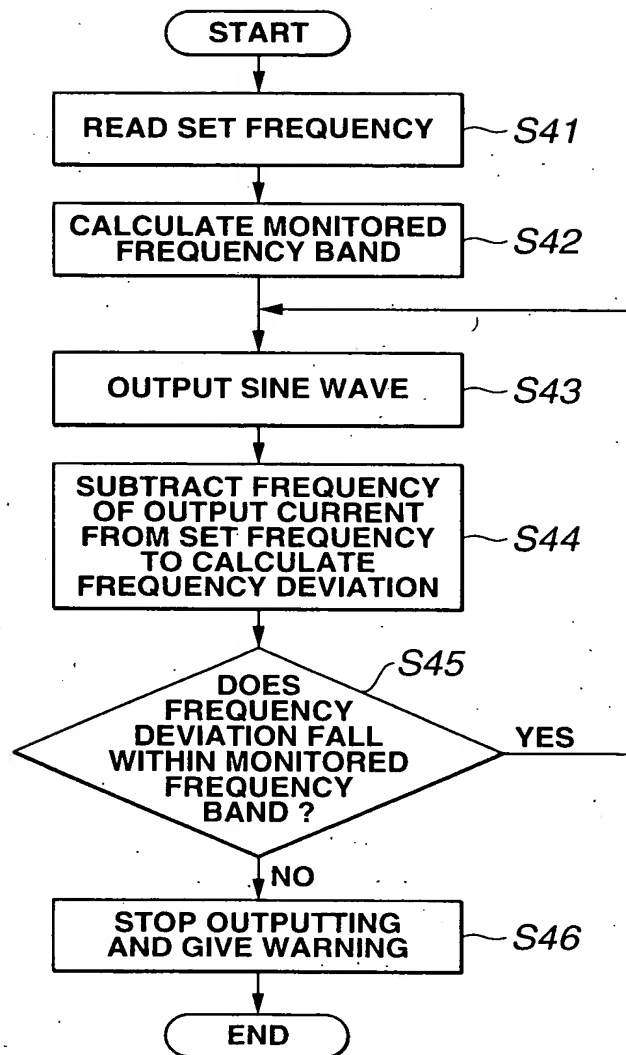


FIG.22

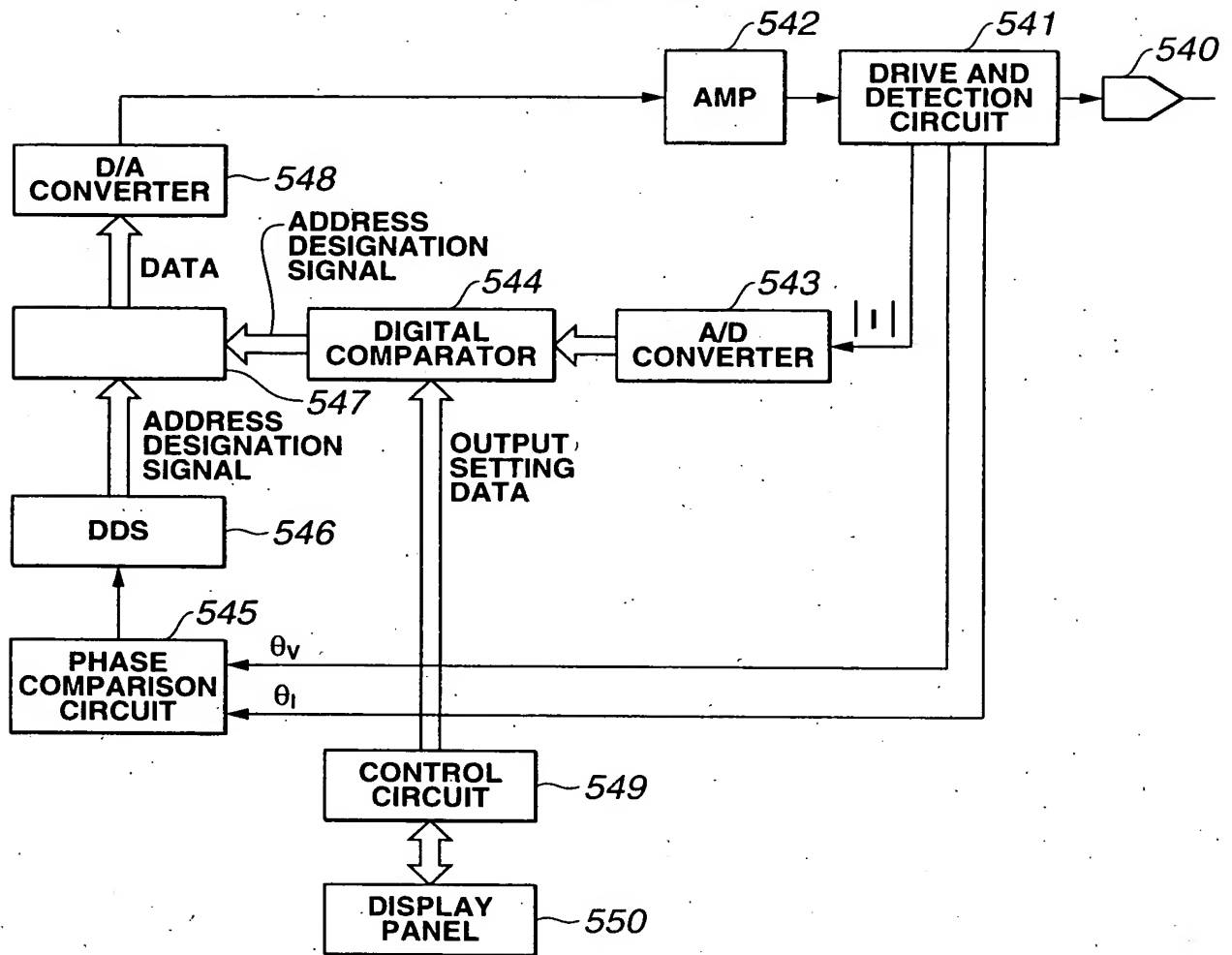


FIG.23

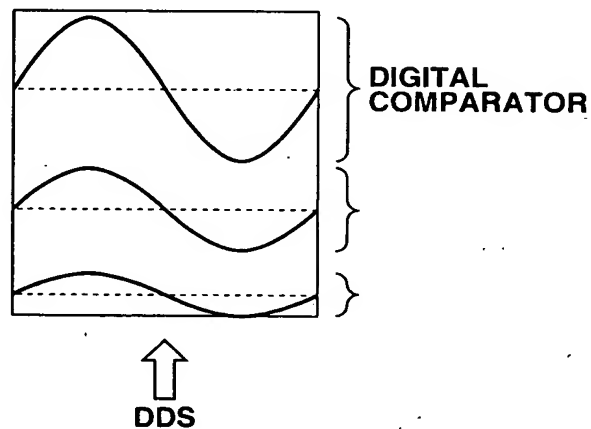


FIG.24
PRIOR ART

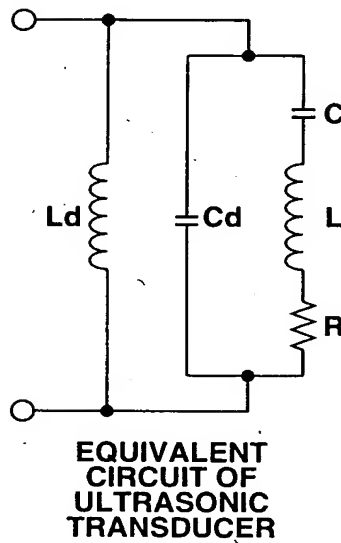


FIG.25
PRIOR ART

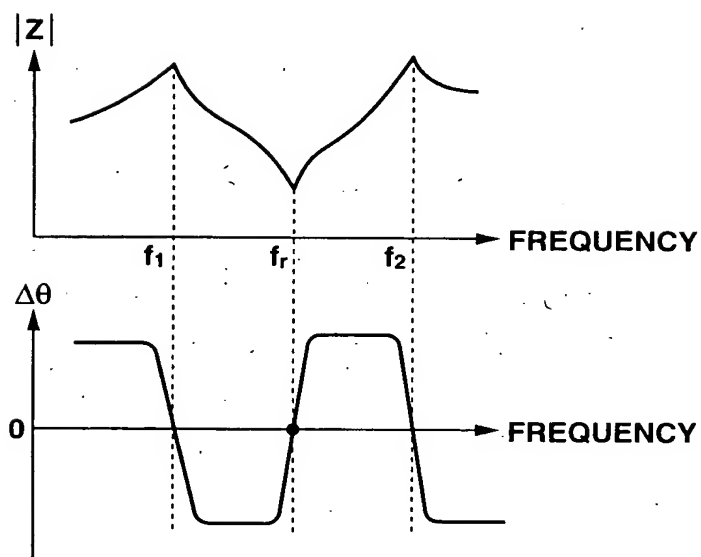


FIG.26
PRIOR ART

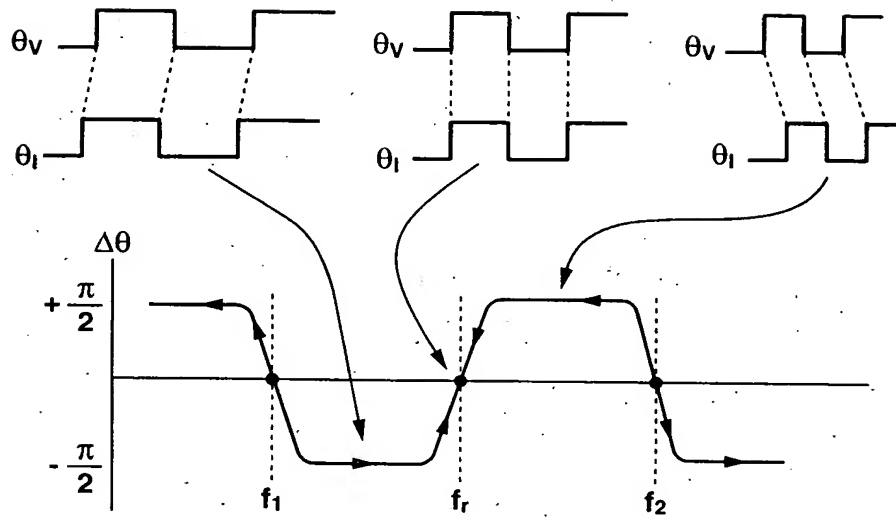


FIG.27
PRIOR ART

